

ABSTRACT OF THE DISCLOSURE

The present invention relates to a memory interlace-checking method and, in particular, to a test method that can effectively detect the weakening of memory.

This test method is different from the conventional continuous address testing style.

It is an interlacing address test method that comprises a main step and a data checking step. The main step provides main data to perform command actions on local addresses in memory. This will weaken other portions in the memory that are not triggered by commands because of the electromagnetic interference (EMI) induced by memory operations. Afterwards, in the data checking step, the yet to be triggered portion will be checked in a complementary way in order to accurately detect weakened memory.